

LM2674 SIMPLE SWITCHER[®] Power Converter High Efficiency 500 mA Step-Down Voltage Regulator

General Description

The LM2674 series of regulators are monolithic integrated circuits built with a LMDMOS process. These regulators provide all the active functions for a step-down (buck) switching regulator, capable of driving a 500 mA load current with excellent line and load regulation. These devices are available in fixed output voltages of 3.3V, 5.0V, 12V, and an adjustable output version.

Requiring a minimum number of external components, these regulators are simple to use and include patented internal frequency compensation (Patent Nos. 5,382,918 and 5,514,947) and a fixed frequency oscillator.

The LM2674 series operates at a switching frequency of 260 kHz, thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Because of its very high efficiency (>90%), the copper traces on the printed circuit board are the only heat sinking needed.

A family of standard inductors for use with the LM2674 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies using these advanced ICs. Also included in the datasheet are selector guides for diodes and capacitors designed to work in switch-mode power supplies.

Other features include a guaranteed $\pm 1.5\%$ tolerance on output voltage within specified input voltages and output load conditions, and $\pm 10\%$ on the oscillator frequency. External shutdown is included, featuring typically 50 µA stand-by current. The output switch includes current limiting, as well as thermal shutdown for full protection under fault conditions. To simplify the LM2674 buck regulator design procedure, there exists computer design software, *LM267X Made Simple* (version 6.0).

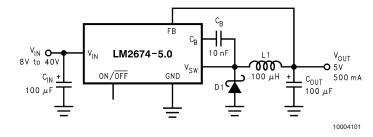
Features

- Efficiency up to 96%
- Available in SO-8, 8-pin DIP and LLP packages
- Computer Design Software LM267X Made Simple (version 6.0)
- Simple and easy to design with
- Requires only 5 external components
- Uses readily available standard inductors
- 3.3V, 5.0V, 12V, and adjustable output versions
- Adjustable version output voltage range: 1.21V to 37V
- ±1.5% max output voltage tolerance over line and load conditions
- Guaranteed 500mA output load current
- 0.25Ω DMOS Output Switch
- Wide input voltage range: 8V to 40V
- 260 kHz fixed frequency internal oscillator
- TTL shutdown capability, low power standby mode
- Thermal shutdown and current limit protection

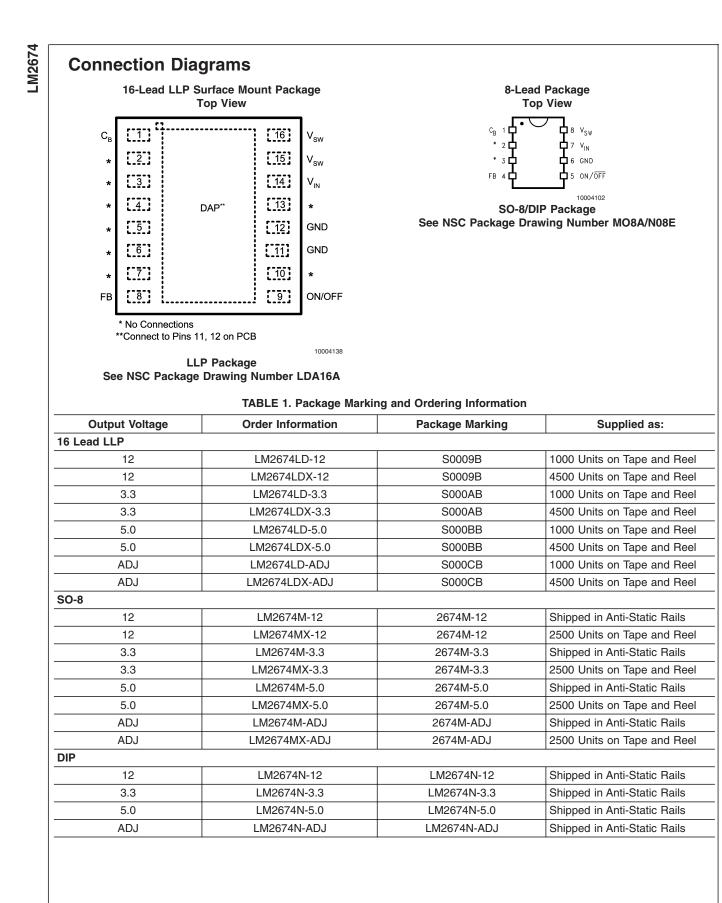
Typical Applications

- Simple High Efficiency (>90%) Step-Down (Buck) Regulator
- Efficient Pre-Regulator for Linear Regulators
- Positive-to-Negative Converter

Typical Application



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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage	45V	Storage Temperature Range	–65°C to +150°C
ON/OFF Pin Voltage	$-0.1V \leq V_{SH} \leq 6V$	Lead Temperature	
Switch Voltage to Ground	-1V	M Package	
Boost Pin Voltage	$V_{SW} + 8V$	Vapor Phase (60s)	+215°C
Feedback Pin Voltage	$-0.3V \le V_{FB} \le 14V$	Infrared (15s)	+220°C
ESD Susceptibility		N Package (Soldering, 10s)	+260°C
Human Body Model (Note 2)	2 kV	LLP Package (See AN-1187)	
Power Dissipation	Internally Limited	Maximum Junction Temperature	+150°C
Operating Ratings			

Supply Voltage6.5V to 40VJunction Temperature Range $-40^{\circ}C \le T_{J} \le +125^{\circ}C$

Electrical Characteristics

LM2674-3.3 Specifications with standard type face are for $T_J = 25^{\circ}C$, and those with **bold type face** apply over **full Operating Temperature Range**.

Symbol	Parameter	Conditions	Typical	Min	Max	Units	
			(Note 4)	(Note 5)	(Note 5)		
SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3)							
V _{OUT}	Output Voltage	V_{IN} = 8V to 40V, I_{LOAD} = 20 mA to 500 mA	3.3	3.251/ 3.201	3.350/ 3.399	V	
V _{OUT}	Output Voltage	V_{IN} = 6.5V to 40V, I_{LOAD} = 20 mA to 250 mA	3.3	3.251/ 3.201	3.350/ 3.399	V	
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 500 \text{ mA}$	86			%	

LM2674-5.0

Symbol	Parameter	Conditions	Typical	Min	Max	Units	
			(Note 4)	(Note 5)	(Note 5)		
SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3)							
V _{OUT}	Output Voltage	$V_{IN} = 8V$ to 40V, $I_{LOAD} = 20$ mA to 500 mA	5.0	4.925/ 4.850	5.075/ 5.150	V	
V _{OUT}	Output Voltage	V_{IN} = 6.5V to 40V, I_{LOAD} = 20 mA to 250 mA	5.0	4.925/ 4.850	5.075/ 5.150	V	
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 500 \text{ mA}$	90			%	

LM2674-12

Symbol	Parameter	Conditions	Typical	Min	Max	Units	
			(Note 4)	(Note 5)	(Note 5)		
SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3)							
V _{OUT}	Output Voltage	V_{IN} = 15V to 40V, I_{LOAD} = 20 mA to 500 mA	12	11.82/ 11.64	12.18/ 12.36	V	
η	Efficiency	$V_{IN} = 24V, I_{LOAD} = 500 \text{ mA}$	94			%	

LM2674-ADJ

Symbol	Parameter	Conditions	Тур	Min	Max	Units	
			(Note 4)	(Note 5)	(Note 5)		
SYSTEM	SYSTEM PARAMETERS Test Circuit Figure 3 (Note 3)						
V_{FB}	Feedback Voltage	$V_{IN} = 8V$ to 40V, $I_{LOAD} = 20$ mA to 500 mA V_{OUT} Programmed for 5V (see Circuit of <i>Figure 3</i>)	1.210	1.192/ 1.174	1.228/ 1.246	v	
V _{FB}	Feedback Voltage	$V_{IN} = 6.5V$ to 40V, $I_{LOAD} = 20$ mA to 250 mA V_{OUT} Programmed for 5V (see Circuit of <i>Figure 3</i>)	1.210	1.192/ 1.174	1.228/ 1.246	v	

LM2674-ADJ	(Continued)
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Symbol	Parameter	Conditions	Typ (Note 4)	Min (Note 5)	Max (Note 5)	Units
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 500 \text{ mA}$	90			%

All Output Voltage Versions

Specifications with standard type face are for $T_J = 25^{\circ}C$, and those with **bold type face** apply over **full Operating Temperature Range**. Unless otherwise specified, $V_{IN} = 12V$ for the 3.3V, 5V, and Adjustable versions and $V_{IN} = 24V$ for the 12V version, and $I_{LOAD} = 100$ mA.

Symbol	Parameters	Conditions	Тур	Min	Max	Units
DEVICE F	PARAMETERS					
l _Q	Quiescent Current	V _{FEEDBACK} = 8V For 3.3V, 5.0V, and ADJ Versions	2.5		3.6	mA
		V _{FEEDBACK} = 15V For 12V Versions	2.5			mA
I _{STBY}	Standby Quiescent Current	ON/OFF Pin = 0V	50		100/ 150	μA
I _{CL}	Current Limit		0.8	0.62/ 0.575	1.2/ 1.25	A
IL	Output Leakage Current	$V_{IN} = 40V, ON/\overline{OFF}$ Pin = 0V $V_{SWITCH} = 0V$	1		25	μA
		$V_{SWITCH} = -1V, ON/\overline{OFF}$ Pin = 0V	6		15	mA
R _{DS(ON)}	Switch On-Resistance	I _{SWITCH} = 500 mA	0.25		0.40/ 0.60	Ω
f _o	Oscillator Frequency	Measured at Switch Pin	260	225	275	kHz
D	Maximum Duty Cycle		95			%
	Minimum Duty Cycle		0			%
I _{BIAS}	Feedback Bias Current	V _{FEEDBACK} = 1.3V ADJ Version Only	85			nA
V _{S/D}	ON/OFF Pin Voltage Thesholds		1.4	0.8	2.0	V
I _{S/D}	ON/OFF Pin Current	ON/OFF Pin = 0V	20	7	37	μΑ
θ_{JA}	Thermal Resistance	N Package, Junction to Ambient (Note 6)	95			°C/W
		M Package, Junction to Ambient (Note 6)	105			

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but device parameter specifications may not be guaranteed under these conditions. For guaranteed specifications and test conditions, see the Electrical Characteristics.

Note 2: The human body model is a 100 pF capacitor discharged through a 1.5 k Ω resistor into each pin.

Note 3: External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator performance. When the LM2674 is used as shown in *Figures 2, 3* test circuits, system performance will be as specified by the system parameters section of the Electrical Characteristics.

Note 4: Typical numbers are at 25°C and represent the most likely norm.

Note 5: All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).

Note 6: Junction to ambient thermal resistance with approximately 1 square inch of printed circuit board copper surrounding the leads. Additional copper area will lower thermal resistance further. See Application Information section in the application note accompanying this datasheet and the thermal model in *LM267X Made Simple* (version 6.0) software. The value θ_{J-A} for the LLP (LD) package is specifically dependent on PCB trace area, trace material, and the number of layers and thermal vias. For improved thermal resistance and power dissipation for the LLP package, refer to Application Note AN-1187.



Physical Dimensions inches (millimeters) unless otherwise noted

